

Features

- High Efficiency: Up to 95%@5V
- 600KHz Frequency Operation
- 2.0A Output Current
- No Schottky Diode Required
- 3.5V to 18V Input Voltage Range
- 0.8V Reference Voltage
- Slop Compensated Current Mode Control for Excellent Line and Load Transient Response
- Integrated internal compensation
- Stable with Low ESR Ceramic Output Capacitors
- Over Current Protection With Hiccup-Mode
- Input over voltage protection(OVP)
- Thermal Shutdown
- Inrush Current Limit and Soft Start

Applications

- Distributed Power Systems
- Digital Set Top Boxes
- Flat Panel Televicion and Monitors
- Notebook Computer
- Wireless and DSL Modems

General Description

The HCR3142 is a fully integrated, high efficiency 2.0A synchronous rectified step-down converter. The HCR3142 operates at high efficiency over a wide output current load range.

This device offers two operation modes, PWM control and PFM Mode switching control, which allows a high efficiency over the wider range of the load.

The HCR3142 requires a minimum number of readily available standard external components and is available in a TSOP-6 (SOT23-6L) ROHS compliant package.



TSOP-6 (SOT23-6L)

Figure 1. Package Type of HCR3142



Pin Configuration

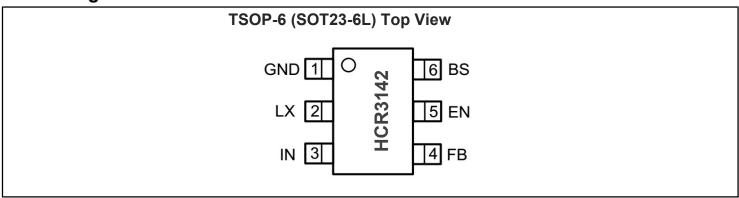
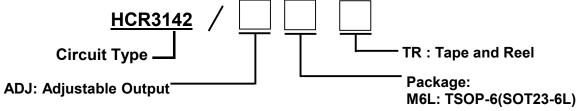


Figure 2. Pin Configuration of HCR3142 (Top View)

Pin Function Table

Pin Number	Pin Name	Function
1	GND	Ground Pin
2	LX	Power Switch Output. It is the switch node connection to inductor. This pin connects to the drains of the internal P-ch and N-ch MOSFET switches.
3	IN	Power Supply input pin.
4	FB	Output Voltage Feedback Pin. Connect FB to the center point of the external resistor divider.
5	EN	Chip Enable Pin. Drive EN pin to a logic-high to enable the IC. Drive EN pin to a logic-Low to disable the IC and enter micro-power shutdown mode. Don't floating this pin.
6	BS	Bootstrap. A capacitor connected between LX and BS pins is required to form a floating supply across the high-side switch driver.

Ordering Information



Ordering Code

Part Number	Marking ID note2	Temperature Range	Package	Quantity per Reel
HCR3142/ADJM6LTR	S42BXXX	-40'C to +85'C	TSOP-6 (SOT23-6L)	3000pcs/TR

Note 2: "S42B" is device code and "XXX" is Inside code.



Absolute Maximum Ratings Note 1

	1		
Parameter	Symbol	Value	Unit
Input Supply Voltage Range	Vin	-0.3 to +20.0	V
LX Voltage Range	VLX	-0.3 to +20.0	V
EN Voltage Range	VEN	-0.3 to +20.0	V
FB Voltage Range	VFB	-0.3 to +6.0	V
BS Voltage Range	VBS	-0.6 to +25.0	V
Power Dissipation	Ро	1000	mW
Thermal Resistance Junction to Ambient	Reja	130.0	'C/W
Thermal Resistance Junction to Case	Rejc	15	'C/W
Storage Temperature Range	Тѕтс	-65 to 150	'C
Operating Junction Temperature	TJ	-40 to +85	'C
Lead Temperature (Soldering, 10s)	TLEAD	260	'C
Human Body Model for all pins	VESD_HBM	±2000	V
Charge Device Model for all pins	VESD_CDM	±400	٧

Note 1: Stresses beyond those listed under"Absolute maximum Ratings" may damage the device.

Recommended Operating Conditions

Parameter	Symbol	Test Condition	Min	Type	Max	Unit
Input Voltage Range	Vin		3.5	1	18	>
Operating Junction Temperature Range	TJ		-40	-	125	'C

^{2:} The device is not guaranteed to function outside the recommended operating conditions.



Electrical Characteristics

(VIN=VEN=12V, VOUT=5V, TA=25'C, unless otherwise noted.)

Parameter	Symbol	Test Condition	Min	Туре	Max	Unit
Input Voltage Range	Vin		3.5	-	18	٧
Input OVP Threshold	V OVP		-	19	-	٧
UVLO Threshold	V uvlo		3.0	-	-	٧
Quiescent Current	lq	VEN=2.0V, IOUT=0, No Load	-	400	600	uA
Shutdown Current	Ishdn	VEN=0V	-	2	-	uA
Regulated Feedback Voltage Accuracy	VREF	Ta=25'C, 3.5V <vin<18v< td=""><td>0.784</td><td>0.800</td><td>0.816</td><td>٧</td></vin<18v<>	0.784	0.800	0.816	٧
High-Side Switch On Resistance	RDS(ON)1		-	120	-	mΩ
Low-Side Switch On Resistance	RDS(ON)2		-	70	-	mΩ
High-Side Switch Leakage Current	llx_lc	VEN=0V, VLX=0V	-	0.01	10	uA
Upper Switch Current Limit	İLIM	Minimum Duty Cycle	-	3.0	-	Α
Oscillation Frequency	Fosc	VFB=0.8V	-	600	-	KHz
Maximum Duty Cycle	η	VFB=0.8V	-	95	-	%
EN High Level Input Voltage	VEN-H		1.5	-	-	V
EN Low Level Input Voltage	VEN-L		-	-	0.3	٧
EN Leakage Current	IEN_LC		-	-	2.0	uA
Minimum On-Time	Ton		-	100	-	nS
Minimum Off-Time	Toff		-	160	-	nS
Soft Start	Tstart		-	800	-	uS
Thermal Shutdown note3	TSHDN		-	160	-	'C
Thermal Hysteresis ^{note3}	Тнүтѕ		-	30	-	'C

Note 3. Thermal shutdown threshold and hysteresis are guaranteed by design.



Typical Performance Characteristics

(Test condition: VIN=12V, VOUT=3.3V, L=4.7uH, TA=25'C, unless otherwise noted.)

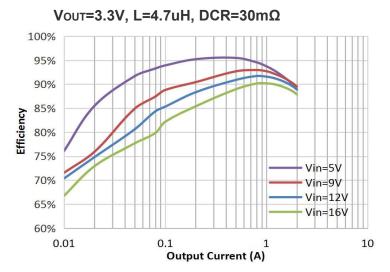


Figure 3. Efficiency at Vout=3.3V

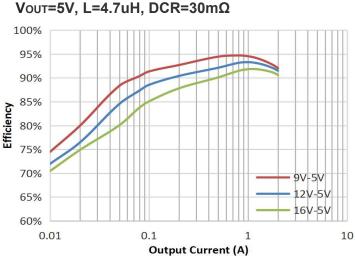


Figure 4. Efficiency at Vout=5.0V

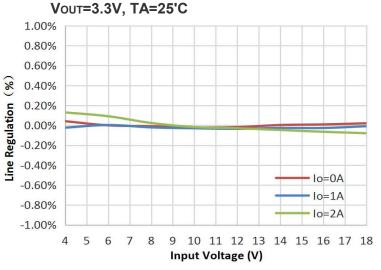


Figure 5. Line Regulation at Vout=3.3V

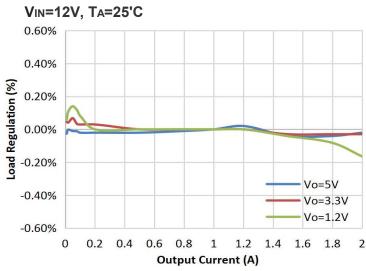


Figure 6. Load Regulation at Vin=12V

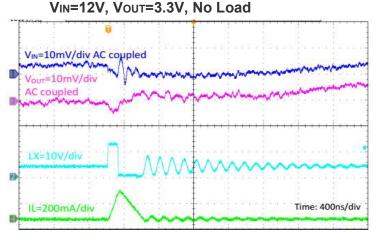


Figure 7. Steady State Operation

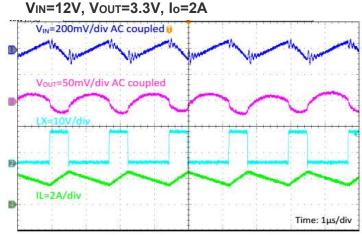
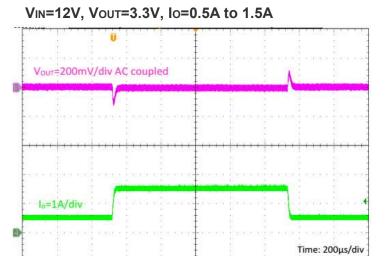


Figure 8. Steady State Operation



Typical Performance Characteristics(Con.)

(Test condition: VIN=12V, VOUT=3.3V, L=4.7uH, TA=25'C, unless otherwise noted.)



VIN=12V, VOUT=3.3V, IO=1.0A to 2A

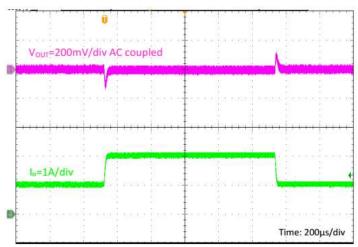
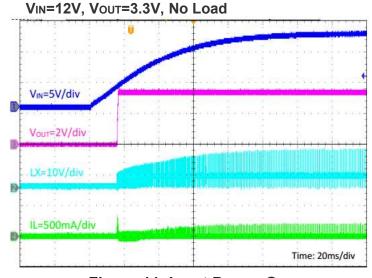


Figure 9. Load Transient

Figure 10. Load Transient



VIN=12V, VOUT=3.3V, No Load

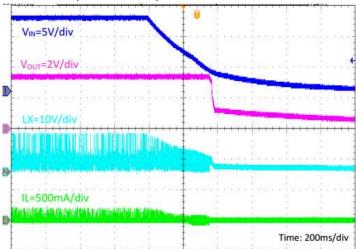
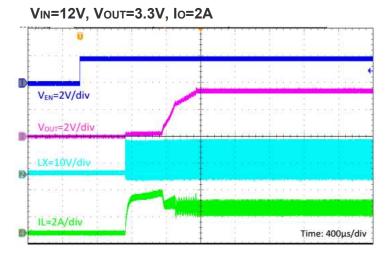


Figure 11. Input Power On

Figure 12. Input Power Down



V_{FN}=2V/div

VIN=12V, VOUT=3.3V, Io=2A

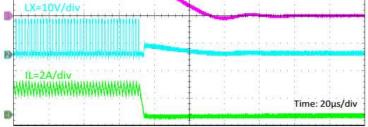


Figure 13. EN Enable Power On

Figure 14. EN Disable Power Down



Typical Performance Characteristics(Con.)

(Test condition: VIN=12V, VOUT=3.3V, L=4.7uH, TA=25'C, unless otherwise noted.)

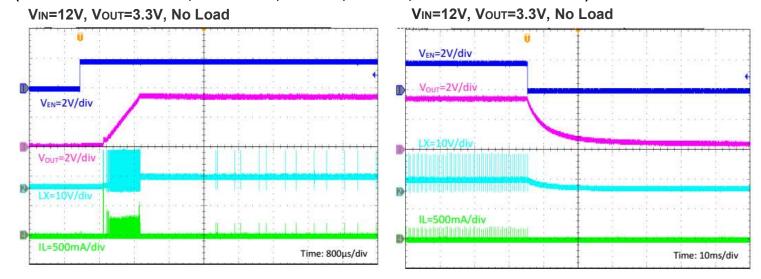


Figure 15. EN Enable Power On

Figure 16. EN Disable Power Down

Functional Block Diagram

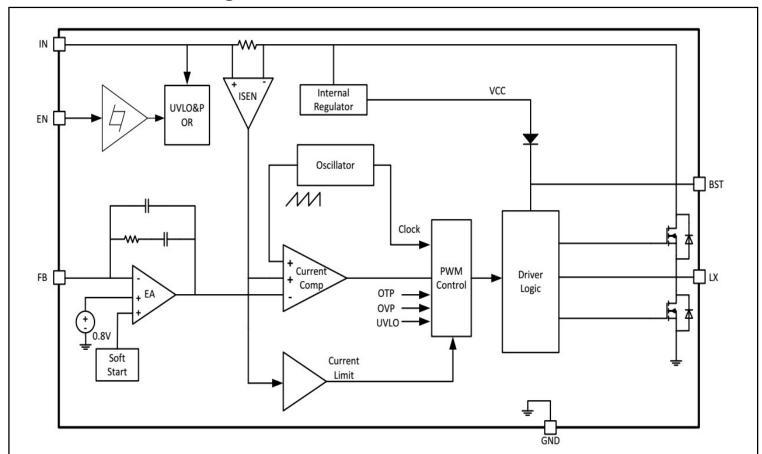


Figure 17. Functional Block Diagram of HCR3142



Operation Description

Internal Regulator

The HCR3142 is a current mode step down DC/DC converter that provides excellent transient response with no extra external compensation components. This device contains an internal/low resistance/high voltage power MOSFET&operates at a high 600KHz operating frequency to ensure a compact, high efficiency design with excellent AC and DC performance.

Error Amplifier

The error amplifier compares the FB pin voltage with the internal FB reference (VFB) and outputs a currentproportional to the difference between the two. This output current is then used to charge or discharge the internal compensation network to form the COMP voltage, which is used to control the power MOSFET current. The optimized internal compensation network minimizes the external component counts and simplifies the control loop design.

Error Amplifier

The soft-start is implemented to prevent the converter output voltage from overshooting during startup. When the chip starts, the internal circuitry generates a softstart voltage(SS) ramping up from 0V to 0.8V. When it is lower than the internal reference(VREF), SS overrides REF so the error amplifier uses SS as the reference. When SS is higher than REF, REF regains control. The SS

Error Amplifier(Con.) time is internally fixed to 0.8ms.

Over-Current-Protection and Hiccup

The HCR3142 has cycle-by-cycle over current limit when the inductor current peak value exceeds the set current limit threshold. Meanwhile, output voltage starts to drop until FB is below the Under-voltage(UV) threshold, typically 55% below the reference. Once a UV is triggered, the HCR3142 enters hiccup mode to periodically restart the part. This protection mode is especially useful when the output is dead-short to ground. The average short circuit current is greatly reduced to alleviate the thermal issue and to protect the regulator. The HCR3142 exits the hiccup mode once the over current condition is removed.

Start up and Shutdown

If both VIN and EN are higher than their appropriate thresholds, the chip starts. The reference block starts first, generating stable reference voltage and currents, and then the internal regulator is enabled. The regulator provides stable supply for the remaining circuits. Three events can shutdown the chip: EN low. VIN low and thermal shutdown. In the shutdown procedure, the signaling path is first blocked to avoid any fault triggering. The COMP voltage and the internal supply rail are then pulled down. The floating this shutdown command.

Basic Application Circuit

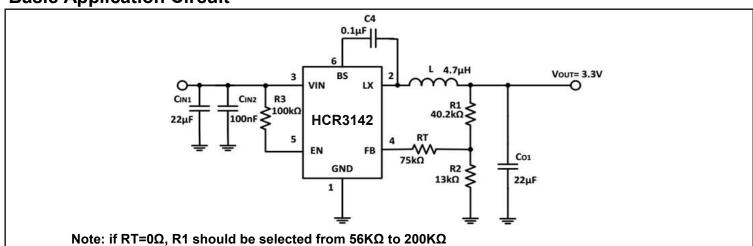


Figure 19. Basic Application Circuit of HCR3142



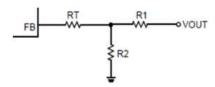
Application Information

Setting the Output Voltage

The external resistor divider is used to set the output voltage(see Figure 19). The feedback resistor R1 also sets the feedback loop bandwidth with the internal compensation capacitor. Choose R1 to be around $10 \text{K}\Omega$ for optimal transient response. R2 is then given by:

 $R_2 = \frac{R_1}{V_{out} / V_{FB} - 1}$

Use a T-type network for when VOUT is low.



VOUT	R1(KΩ)	R2(KΩ)	RT(KΩ)
5V	40.2	7.26	75
3.3V	40.2	12.13	75
2.5V	40.2	17.73	100
1.8V	40.2	29.71	120
1.2V	20.5	36.05	249
1.05V	10	26.84	300

Inductor Selection

A DC current rating of at least 25% percent higher than the maximum load current is recommended for most applications. Inductance value is related to inductor ripple current value, input voltage, output voltage setting and switching frequency. The inductor value can be derived from the following equation:

$$L = \frac{V_{OUT} \times (V_{IN} - V_{OUT})}{V_{IN} \times \Delta I_{I} \times f_{OSC}}$$

Where ΔIL is inductor ripple current. Large value inductors result in lower ripple current & small value inductors result in high ripple current, So inductor value has effect on output voltage ripple value. DC resistance of inductor which has impact on efficiency of DC/DC converter should be taken into account when selecting the inductor.

The maximum inductor peak current is:

$$I_{L(MAX)} = I_{LOAD} + \frac{\Delta I_L}{2}$$

Under light load conditions below 100mA, larger inductance is recommended for improved efficiency.

Output Capacitor Selection

The output capacitor(Co1) is required to maintain the DC output voltage. Ceramic, tantalum, or low ESR electrolytic capacitors are recommended. Low ESR capacitors are preferred to keep the output voltage ripple low. The output voltage ripple can be estimated by:

 $\Delta V_{OUT} = \frac{V_{OUT}}{f_S \times L} \times \left[1 - \frac{V_{OUT}}{V_{IN}} \right] \times \left[R_{ESR} + \frac{1}{8 \times f_S \times C_2} \right]$

where L is the inductor value and RESR is the equivalent series resistance(ESR) value of the output capacitor. In the case of ceramic capacitors, the impedance at the switching frequency is dominated by the capacitance.

the output voltage ripple is mainly caused by the capacitance. For simplification, the output voltage ripple can be estimated by:

$$\Delta V_{OUT} = \frac{V_{OUT}}{8 \times f_S^2 \times L \times C_2} \times \left[1 - \frac{V_{OUT}}{V_{IN}} \right]$$

In the case of tantalum or electrolytic capacitors, the ESR dominates the impedance at the switching frequency. For simplification, the output ripple can be approximated to

approximated to $\Delta V_{OUT} = \frac{V_{OUT}}{f_{S} \times L} \times \left[1 - \frac{V_{OUT}}{V_{IN}}\right] \times R_{ESR}$

The characteristics of the output capacitor also affect the stability of the regulation system. The HCR3142 can be optimized for a wide range of capacitance and ESR values.

Lavout Consideration

when laying out the printed circuit board, the following checking should be used to ensure proper operation of the HCR3142. Check the following in your layout:

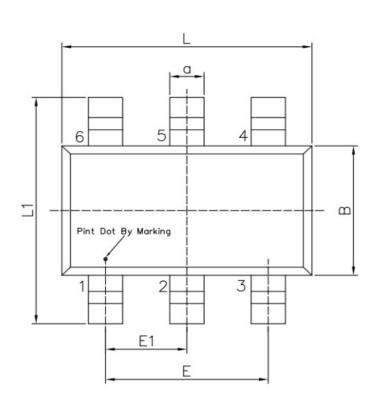
- 1.1) Keep the path of switching current short and minimize the loop area formed by input capacitor, high-side MOSFET and low-side MOSFET.
- 1.2) Bypass ceramic capacitors are suggested to be put close to the IN Pin.
- 1.3) Ensure all feedback connections are short and direct. Place the feedback resistors and compensation as close to the chip as possible.
- 1.4) VOUT, LX away from sensitive analog areas as FB.
- 1.5) Connect IN, LX, and especially GND respectively to a large copper area to cool the chip to improve thermal performance and long-term reliability.

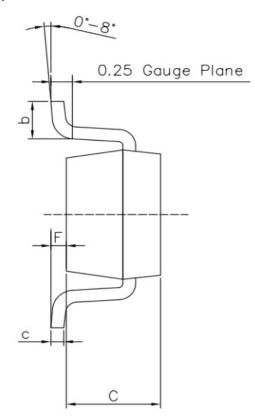


Mechanical Dimensions

M6L PKG: TSOP-6 (SOT23-6L)

Unit: mm





Cumhal	Dimensions I	n Millimeters	Complete	Dimensions In Millimeters		
Symbol	Min	Max	Symbol	Min	Max	
L	2.82	3.02	E1	0.85	1.05	
В	1.50	1.70	a	0.35	0.50	
С	0.90	1.30	С	0.10	0.20	
L1	2.60	3.00	b	0.35	0.55	
Е	1.80	2.00	F	0	0.15	

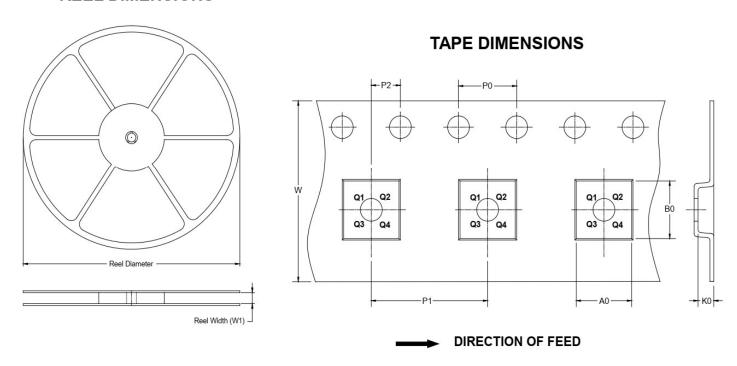
Note:

- 1) All dimensions are in millimeters.
- 2) Package length does not include mold flash, protrusion or gate burr.
- 3) Package width does not include inter lead flash or protrusion.
- 4) Lead popularity (bottom of leads after forming) shall be 0.10 millimeters max.
- 5) Pin 1 is lower left pin when reading top mark from left to right.



TAPE AND REEL INFORMATION

REEL DIMENSIONS



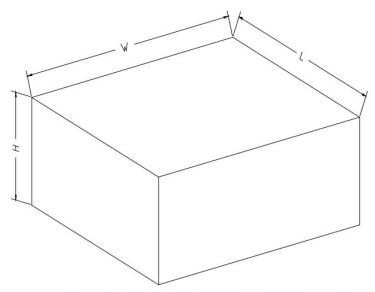
NOTE: The picture is only for reference. Please make the object as the standard.

KEY PARAMETER LIST OF TAPE AND REEL

Package Type	Reel Diameter	Reel Width W1	Α0	В0	K0	P0	P1	P2	W	Pin1
		(mm)	(mm)	(mm)	(mm)	(mm)	(mm)	(mm)	(mm)	Quadrant
TSOP-6 (SOT23-6L)	7"	9.5	3.20	3.20	1.40	4.0	4.0	2.0	8.0	Q3



CARTON BOX DIMENSIONS



NOTE: The picture is only for reference. Please make the object as the standard.

KEY PARAMETER LIST OF CARTON BOX

Reel Type	Length (mm)	Width (mm)	Height (mm)	Pizza/Carton
7"	442	410	224	18