

## Dual 4-Channel Analog Multiplexer/Demultiplexer

## Features

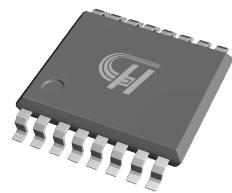
- \* Wide Analog Input Voltage Range:  
From 3V to +18V
  - \* Fully Static Operation
  - \* 5V, 10V and 18V Parametric Ratings
  - \* Standardized Symmetrical Output
- Characteristics
- \* Specified From -40°C to +125°C
  - \* Available in SOIC-16/TSSOP16 Package

## General Description

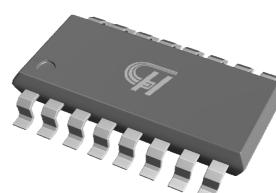
The HCR4052H is a dual single-pole quad-throw analog switch (2XSP4T) suitable for use in analog or digital 4:1 multiplexer/demultiplexer applications. Each switch features four independent inputs/outputs (nY0, nY1, nY2 and nY3) and a common input/output (nZ). A digital enable input ( $\overline{E}$ ) and two digital select input (S0 and S1) are common both switches. When  $\overline{E}$  is HIGH, the switches are turned off. Inputs include clamp diodes. This enables the use of current limiting resistors to interface inputs to voltages in excess of VCC.

## Applications

- \* Analog Multiplexing and demultiplexing
- \* Digital Multiplexing and demultiplexing
- \* Signal Gating



TSSOP-16



SOIC-16

Figure 1. Package Type of HCR4052H

## Dual 4-Channel Analog Multiplexer/Demultiplexer

### Pin Configuration

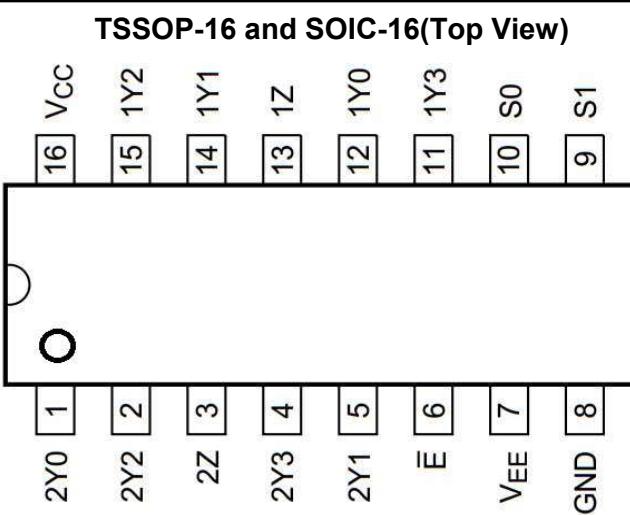


Figure 2. Pin Configuration of HCR4052H (Top View)

### Pin Function Table

Pin	NAME	Description
TSSOP-16 SOIC-16		
1	2Y0	Independent input or output
2	2Y2	Independent input or output
3	2Z	Common input or output
4	2Y3	Independent input or output
5	2Y1	Independent input or output
6	$\bar{E}$	Enable input (active LOW)
7	VEE	Negative Supply Voltage
8	GND	Ground Supply Voltage
9	S1	Select Logic Input
10	S0	Select Logic Input
11	1Y3	Independent input or output
12	1Y0	Independent input or output
13	1Z	Common input or output
14	1Y1	Independent input or output
15	1Y2	Independent input or output
16	VCC	Positive Supply Voltage

## Dual 4-Channel Analog Multiplexer/Demultiplexer

### LOGIC SYMBOL

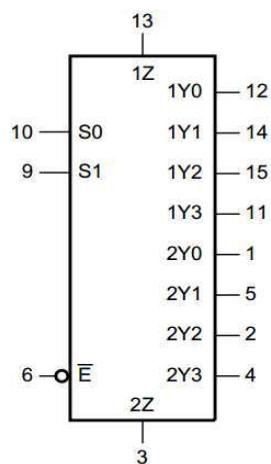


Figure 3-1. Logic Symbol

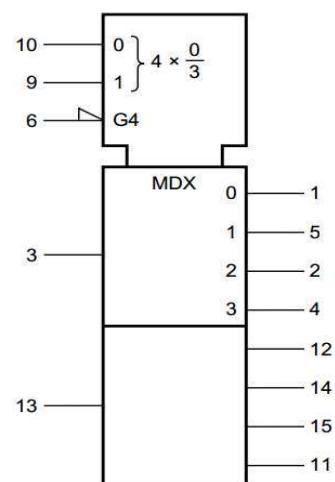


Figure 3-2. IEC Logic Symbol

### Schematic Diagram (one switch)

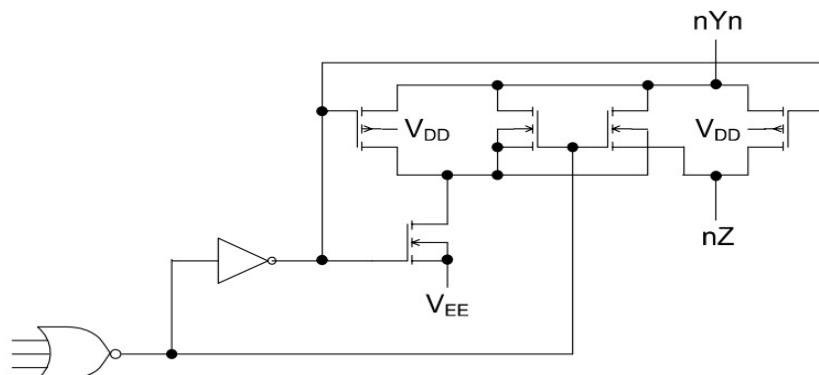


Figure 4. Schematic Diagram (one switch)

### Function Diagram

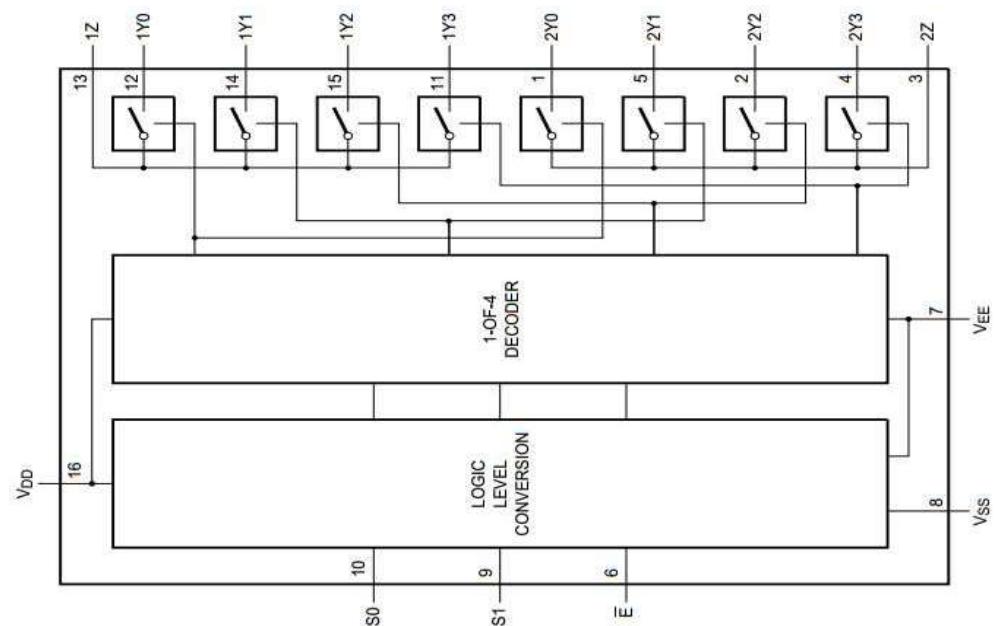
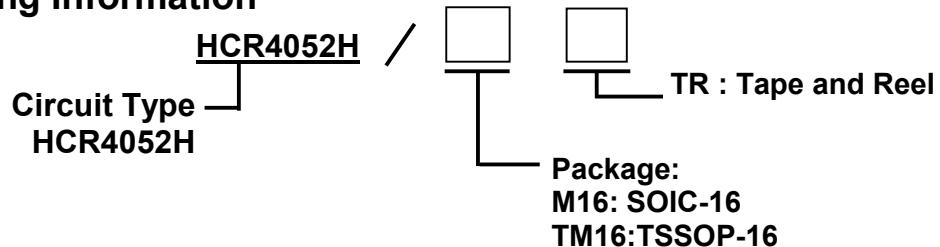


Figure 5. Functional Diagram

## Dual 4-Channel Analog Multiplexer/Demultiplexer

### Ordering Information



### Ordering Code

Part Number	Marking	Temperature Range	Package	Quantity per Reel
HCR4052H/M16TR	HCR4052H	-40°C to +125°C	SOIC-16	4000pcs /TR
HCR4052H/TM16TR	HCR4052H	-40°C to +125°C	TSSOP-16	5000pcs /TR

Note: If the physical information is inconsistent with the ordering information,

Please refer to the actual product

### Functional Table

Input			Channel ON
$\bar{E}$	S2	S1	
L	L	L	nY0 to nZ
L	L	H	nY1 to nZ
L	H	L	nY2 to nZ
L	H	H	nY3 to nZ
H	X	X	switches off

Note: H=HIGH Voltage Level;

L=LOW Voltage Level.

X=don't care.

## Dual 4-Channel Analog Multiplexer/Demultiplexer

### ELECTRICAL PARAMETER

#### Absolute Maximum Ratings

(Tamb=25°C, Voltages are referenced to VSS=GND (ground), unless otherwise specified.)

Parameter	Symbol	Test Condition	Min	Type	Max	Unit
Supply Voltage	Vcc	- [1]	-0.5	-	+21.0	V
Supply Voltage	VEE	referenced to VCC	-21	-	+0.5	V
Input Voltage	VI	-	-0.5	-	Vcc+0.5	V
Input Clamping Current	Iik	VI<-0.5V to VI>Vcc+0.5V	-	-	±10	mA
Input/output Current	Iio	-	-	-	±10	mA
Supply Current	ICC	-	-	-	+50	mA
Ground Current	IGND	-	-50	-	-	mA
Total Power Dissipation	Ptot	-	-	-	500	mW
Device Dissipation	P	per output transistor	-	-	100	mW
Storage temperature	Tstg		-65	-	+150	'C
Soldering Temperature	TL	10S	-	-	260	'C

Note 1: To avoid drawing VCC current of terminal nZ, when switch current flows into terminals nYn, the voltage drop across the bidirectional switch must not exceed 0.4V. If the switch current flows into terminal nZ, no VCC current will flow out of terminals nYn, and in this case there is no limit. For the voltage drop across the switch, but the voltages- at nYn and nZ may not exceed VCC or VEE.

#### Recommended Operating Conditions

Parameter	Symbol	Conditions	Min.	Typ.	Max.	UNIT
Supply Voltage	Vcc	-	3.0	-	18.0	V
Supply Voltage	VEE	-	-15.0	-	0	V
Supply Voltage	VCC-VEE	-	3.0	-	18.0	V
Input Voltage	VI	-	0	-	Vcc	V
Ambient temperature	Tamb	in free air	-40	-	+125	'C
Input transition rise and fall rate	$\Delta t/\Delta V$	Vcc=5V	-	-	3.75	us/V
		Vcc=10V	-	-	0.5	us/V
		Vcc=18V	-	-	0.08	us/V

## Dual 4-Channel Analog Multiplexer/Demultiplexer

### ELECTRICAL CHARACTERISTICS

#### DC Characteristics - 1

(Tamb= -40°C ~ +85°C, Voltages are referenced to GND(ground=0V), unless otherwise specified.)

Parameter	Symbol	Test Condition (V)	Tamb=25°C			Unit
			Min	Type	Max	
HIGH-Level Input Voltage	VIH	Ilo  <1uA	Vcc=5V	3.5	-	-
			Vcc=10V	7.0	-	-
			Vcc=18V	12.6	-	-
LOW-Level Input Voltage	VIL	Ilo  <1uA	Vcc=5V	-	-	1.5
			Vcc=10V	-	-	3.0
			Vcc=18V	-	-	5.4
Supply current	Icc	Io=0A	Vcc=5V	-	-	5
			Vcc=10V	-	-	10
			Vcc=18V	-	-	20
Input Leakage Current	Ii	Vcc=18V		-	-	±1.0
OFF-state Leakage Current	Is(OFF)	Vcc=18V	Z port; all channels off; see Figure 6	-	-	±1.0
			Y port; all channels off; see Figure 7	-	-	±1.0
ON Resistance (Peak)	RON(peak)	Vi=0V to Vcc-Vee; see Figure 8 & Figure 9	VCC-VEE=5V	-	117	836
			VCC-VEE=10V	-	58	178
			VCC-VEE=15V	-	41	120
			VCC-VEE=20V	-	36	105
ON Resistance (Rail)	RON(rail)	Vi=0V; see Figure 8 & Figure 9	VCC-VEE=5V	-	44	130
			VCC-VEE=10V	-	29	93
			VCC-VEE=15V	-	24	69
			VCC-VEE=20V	-	22	63
		Vi=VCC-VEE; see Figure 8 & Figure 9	VCC-VEE=5V	-	82	249
			VCC-VEE=10V	-	51	157
			VCC-VEE=15V	-	41	127
			VCC-VEE=20V	-	36	112
ON Resistance Mismatch Between Channels	ΔRON	Vi=0V to Vcc-Vee; see Figure 8	VCC-VEE=5V	-	25	-
			VCC-VEE=10V	-	10	-
			VCC-VEE=15V	-	5	-
			VCC-VEE=20V	-	5	-
Input Capacitance	Ci	Sn, E Inputs		-	-	7.5
pF						

## Dual 4-Channel Analog Multiplexer/Demultiplexer

### ELECTRICAL CHARACTERISTICS

#### DC Characteristics - 2

(Tamb= -40°C ~ +125°C, VSS=VEE=0V; VI=VSS or VCC, unless otherwise specified.)

Parameter	Symbol	Test Condition (V)	Tamb=-40°C		Tamb=+85°C		Tamb=+125°C		Unit	
			Min	Max	Min	Max	Min	Max		
HIGH-Level Input Voltage	VIH	I lo  <1uA	Vcc=5V	3.5	-	3.5	-	3.5	-	V
			Vcc=10V	7.0	-	7.0	-	7.0	-	
			Vcc=18V	12.6	-	12.6	-	12.6	-	
LOW-Level Input Voltage	VIL	I lo  <1uA	Vcc=5V	-	1.5	-	1.5	-	1.5	V
			Vcc=10V	-	3.0	-	3.0	-	3.0	
			Vcc=18V	-	5.4	-	5.4	-	5.4	
Supply current	ICC	Io=0A	Vcc=5V	-	5	-	150	-	150	uA
			Vcc=10V	-	10	-	300	-	300	uA
			Vcc=18V	-	20	-	600	-	600	uA
Input Leakage Current	I <sub>II</sub>	Vcc=18V	-	±1.0	-	±1.0	-	±1.0	uA	

#### AC Characteristics - 1

(Tamb= +25°C, VSS=VEE=0V, unless otherwise specified.)

Parameter	Symbol	Test Condition (V)	Min.	Typ.	Max.	Unit	
HIGH to LOW propagation delay time	tPHL	nYn, nZ to nZ, nYn; see Figure 11	Vcc=5V	-	10	20	ns
			Vcc=10V	-	5	10	
			Vcc=18V	-	5	10	
		Sn to nYn, nZ; see Figure 12	Vcc=5V	-	150	305	ns
			Vcc=10V	-	65	135	
			Vcc=18V	-	50	100	
LOW to HIGH propagation delay time	tPLH	nYn, nZ to nZ, nYn; see Figure 11	Vcc=5V	-	10	20	ns
			Vcc=10V	-	5	10	
			Vcc=18V	-	5	10	
		Sn to nYn, nZ; see Figure 12	Vcc=5V	-	150	300	ns
			Vcc=10V	-	75	150	
			Vcc=18V	-	50	100	
HIGH to OFF-state propagation delay	tPHZ	E to nYn, nZ; see Figure 13	Vcc=5V	-	95	190	ns
			Vcc=10V	-	90	180	
			Vcc=18V	-	85	180	

## Dual 4-Channel Analog Multiplexer/Demultiplexer

### ELECTRICAL CHARACTERISTICS

#### AC Characteristics - 1

(Tamb= +25°C, VSS=VEE=0V, unless otherwise specified.)

Parameter	Symbol	Test Condition (V)	Min.	Typ.	Max.	Unit
LOW to OFF-state propagation delay	tPLZ	$\bar{E}$ to nYn, nZ; see Figure 13	Vcc=5V	-	100	205
			Vcc=10V	-	90	180
			Vcc=18V	-	90	180
OFF-state to HIGH propagation delay	tPZH	$\bar{E}$ to nYn, nZ; see Figure 13	Vcc=5V	-	130	260
			Vcc=10V	-	55	115
			Vcc=18V	-	45	85
OFF-state to LOW propagation delay	tPZL	$\bar{E}$ to nYn, nZ; see Figure 13	Vcc=5V	-	120	240
			Vcc=10V	-	50	100
			Vcc=18V	-	35	75

#### AC Characteristics - 2

(Tamb= +25°C, VSS=VEE=0V, VI=0.5VDD(P-P), unless otherwise specified.)

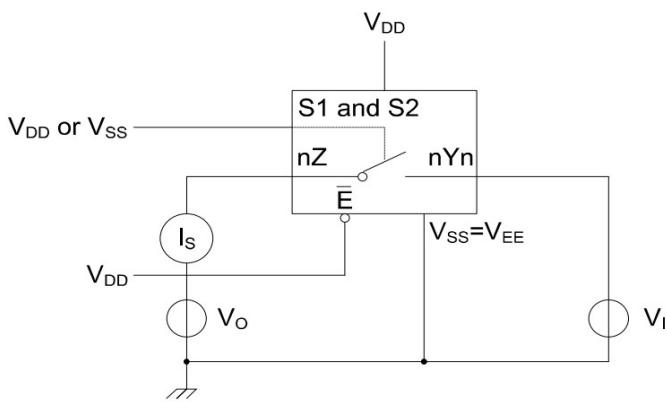
Parameter	Symbol	Test Condition (V)	Min.	Typ.	Max.	Unit
total harmonic distortion	THD	see Figure 14; RL=10KΩ; CL=15pF; channel ON; fi=1KHz	Vcc=5V	-	0.25	-
			Vcc=10V	-	0.04	-
			Vcc=18V	-	0.04	-
-3dB frequency response	f(-3dB)	see Figure 15; RL=1KΩ; CL=5pF; channel ON;	Vcc=5V	-	13	-
			Vcc=10V	-	40	-
			Vcc=18V	-	70	-
isolation (OFF-state)	aiso	see Figure 16; fi=1MHz; RL=1KΩ; CL=5pF; channel OFF; VCC=10V	-	-50	-	dB
crosstalk voltage	Vct	digital inputs to switch; see Figure 17; RL=10KΩ; CL=15pF; $\bar{E}$ or Sn=VCC (square-wave); VCC=10V	-	50	-	mV
crosstalk	Xtalk	between switches; see Figure 18; fi=1MHz; RL=1KΩ; VCC=10V	-	-50	-	dB

Note; 【1】 fi is biased at 0.5VCC; VI=0.5VCC(P-P).

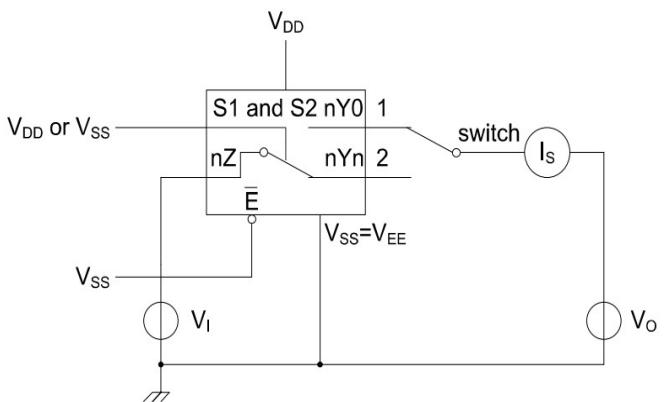
## Dual 4-Channel Analog Multiplexer/Demultiplexer

### Testing Circuit

#### DC Testing Circuit-1

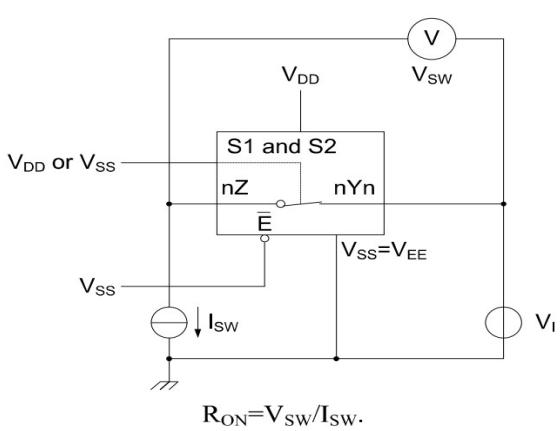


**Figure 6.** Test circuit for measuring OFF-state leakage current Z port

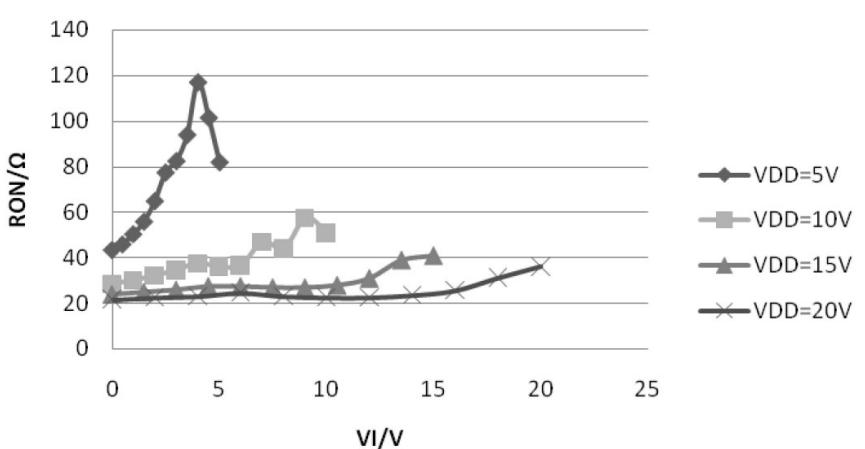


**Figure 7.** Test circuit for measuring OFF-state leakage current nYn port

#### ON Resistance Testing Circuit

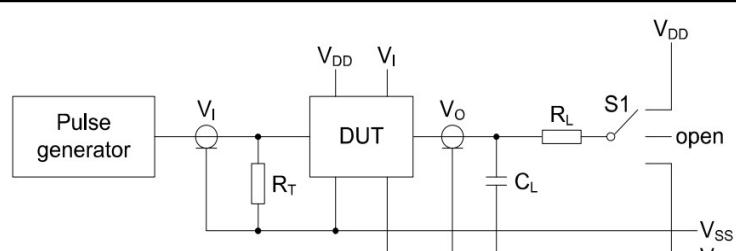


**Figure 8.** Test circuit for measuring Ron



**Figure 9.** Typical Ron as a function of input voltage

#### AC Testing Circuit-1



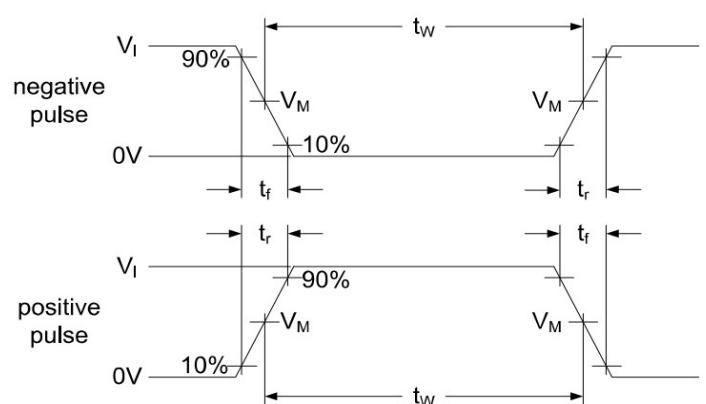
Definitions for test circuit:

DUT=Device Under Test.

CL=Load capacitance including jig and probe capacitance

RT=Termination resistance should be equal to the output impedance Zo of the pulse generator.

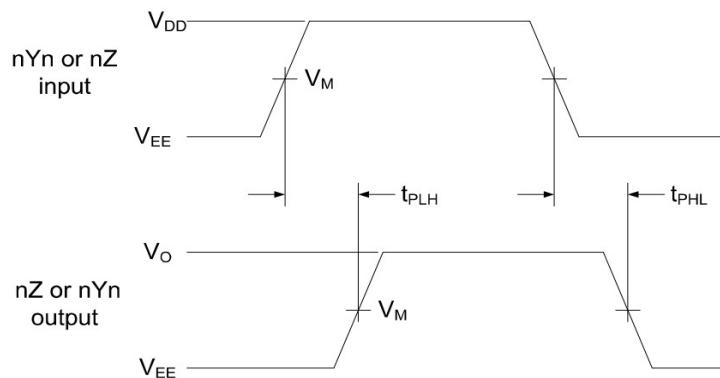
RL=Load resistance.



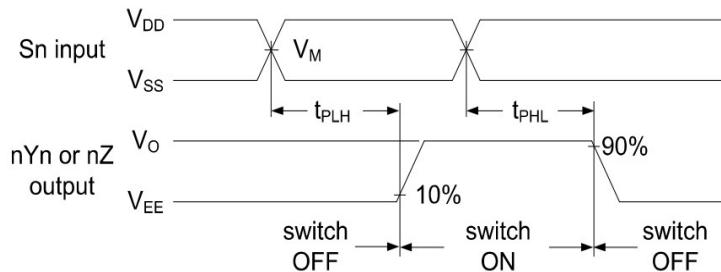
**Figure 10.** Test circuit for switching times

## Dual 4-Channel Analog Multiplexer/Demultiplexer

### AC Testing Waveforms

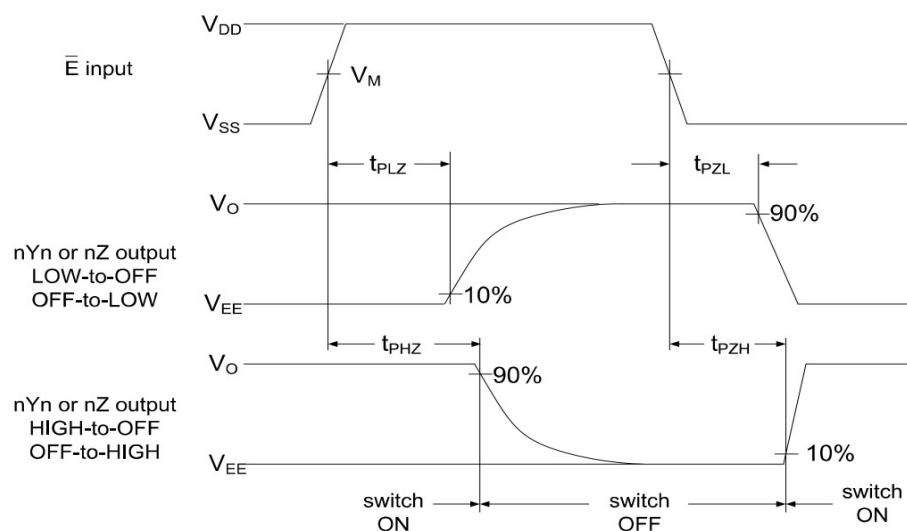


**Figure 11. nYn, nZ to nZ, nYn Propagation delays**



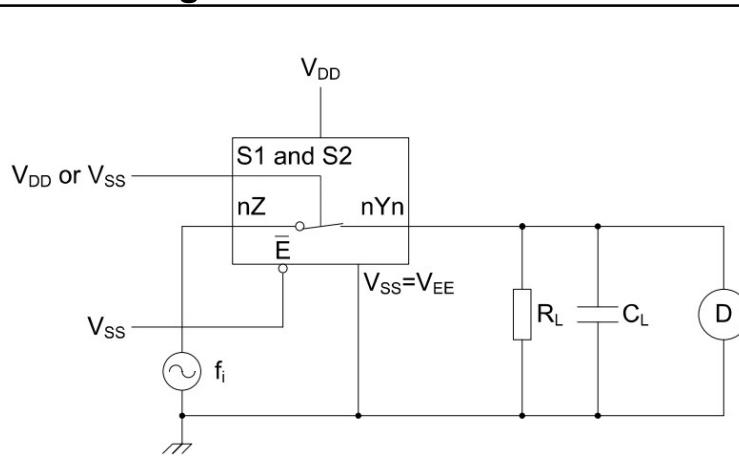
**Figure 12. Sn to nYn, nZ propagation delays**

### ON Resistance Testing Circuit

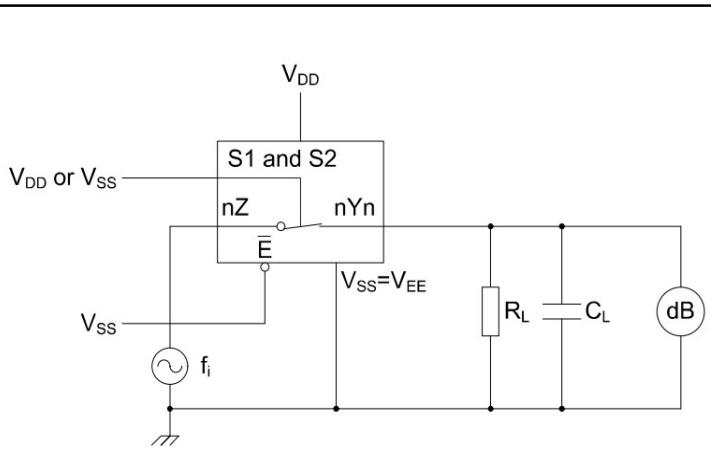


**Figure 13. Enable and disable times**

### AC Testing Circuit-2



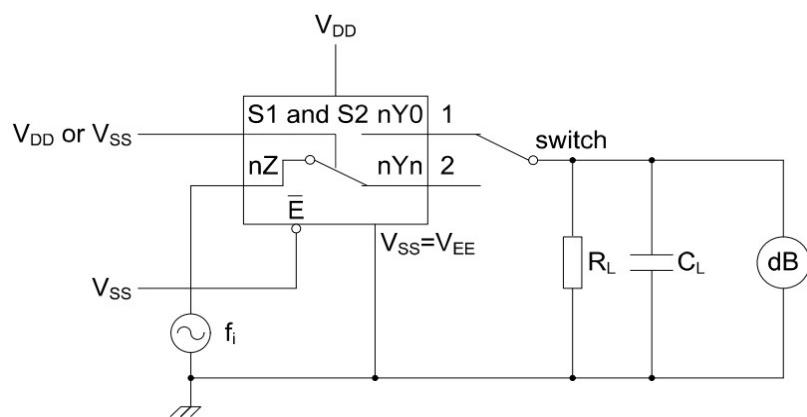
**Figure 14. Test circuit for measuring total harmonic distortion**



**Figure 15. Test circuit for measuring frequency response**

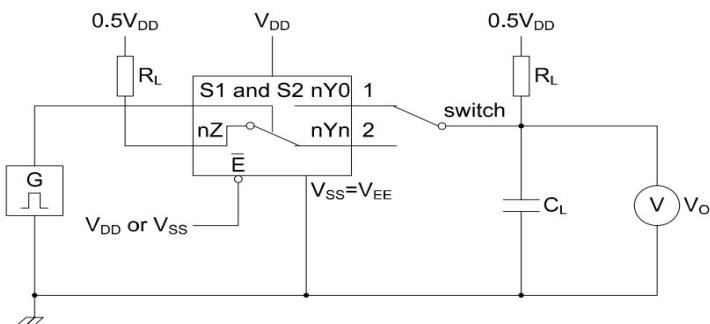
## Dual 4-Channel Analog Multiplexer/Demultiplexer

### AC Testing Circuit-2 (Con.)

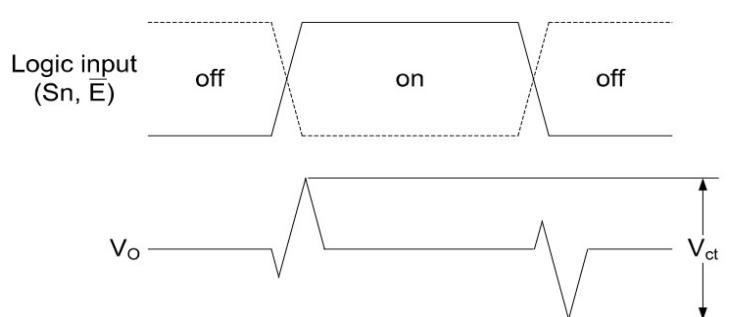


**Figure 16.** Test circuit for measuring isolation (OFF-state)

#### a. Test Circuit

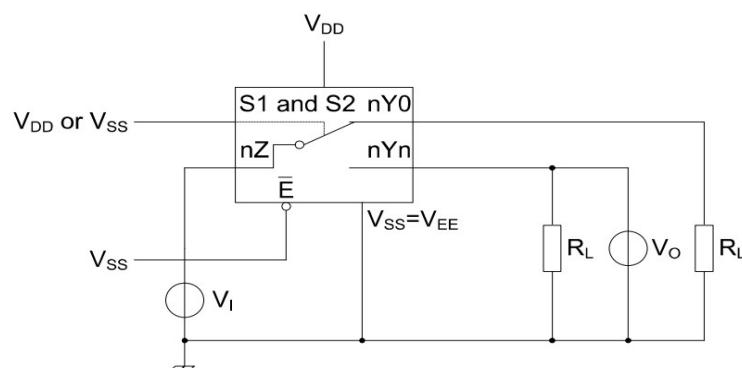


#### b. Input and output pulse definitions

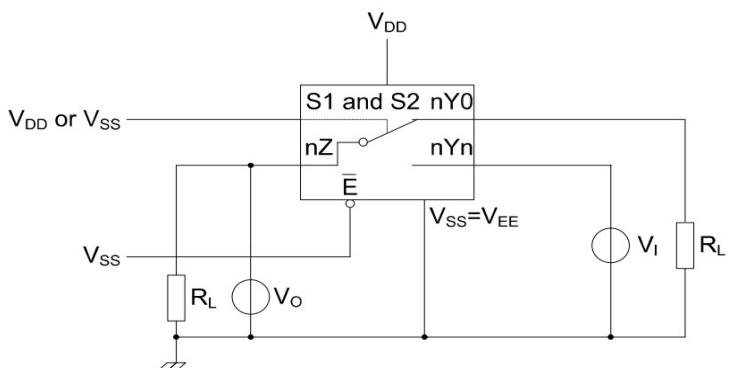


**Figure 17.** Test circuit for measuring crosstalk voltage between digital inputs and switch

#### a. Switch close condition



#### b. Switch open condition



**Figure 18.** Test circuit for measuring crosstalk between switches

## Dual 4-Channel Analog Multiplexer/Demultiplexer

### Measurement Points

Supply Voltage	Input	Output
VCC	VM	VM
5V to 18V	0.5 X VCC	0.5 X VCC

### Test Data

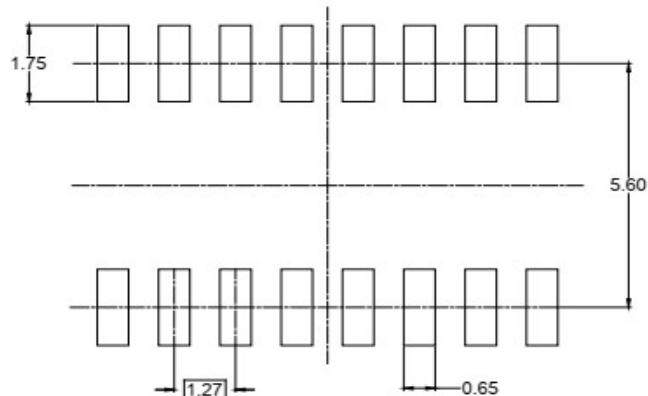
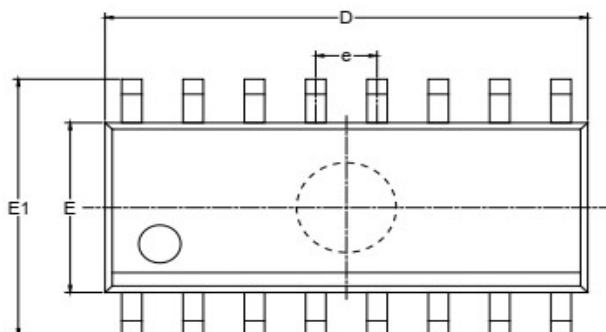
Input				Load		S1 position				
nYn, nZ	Sn and $\overline{E}$	tr, tf	VM	CL	RL	tPHL	tPLH	tPZH, tPHZ	tPZL, tPLZ	other
Vcc or VEE	Vcc or Vss	$\leq 20\text{ns}$	0.5X Vcc	50pF	10K $\Omega$	Vcc or VEE	VEE	VEE	Vcc	VEE

## Dual 4-Channel Analog Multiplexer/Demultiplexer

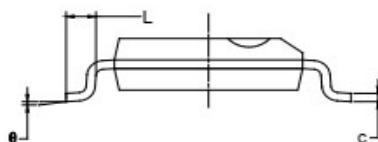
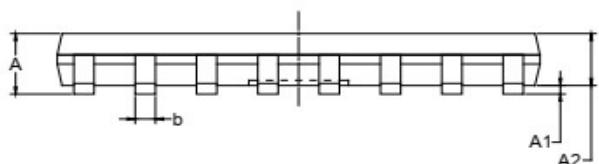
### MECHANICAL DIMENSIONS.

PKG: SOIC-16 ( M16)

unit:mm



RECOMMENDED LAND PATTERN (Unit: mm)



Symbol	Dimensions In Millimeters		Dimensions In Inches	
	MIN	MAX	MIN	MAX
A	1.350	1.750	0.053	0.069
A1	0.100	0.250	0.004	0.010
A2	1.350	1.550	0.053	0.061
b	0.330	0.510	0.013	0.020
c	0.170	0.250	0.006	0.010
D	9.800	10.200	0.386	0.402
E	3.800	4.000	0.150	0.157
E1	5.800	6.200	0.228	0.244
e	1.27 BSC		0.050 BSC	
L	0.400	1.270	0.016	0.050
θ	0°	8°	0°	8°

#### NOTES:

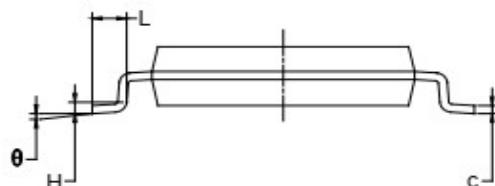
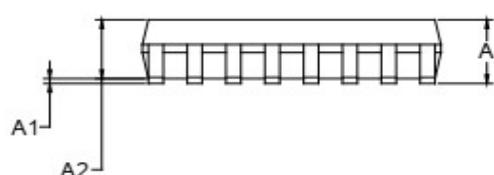
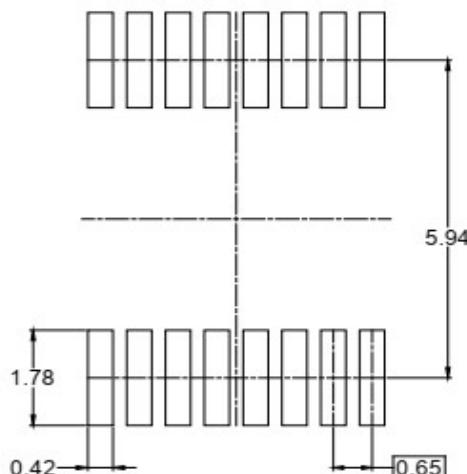
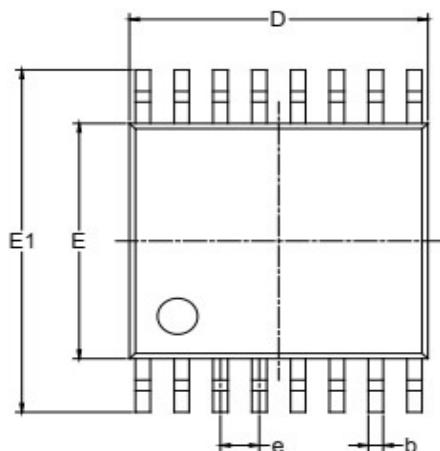
1. Body dimensions do not include mode flash or protrusion.
2. This drawing is subject to change without notice.

## Dual 4-Channel Analog Multiplexer/Demultiplexer

### MECHANICAL DIMENSIONS(Con.)

TM16 PKG: TSSOP-16

unit:mm



Symbol	Dimensions In Millimeters		Dimensions In Inches	
	MIN	MAX	MIN	MAX
A		1.200		0.047
A1	0.050	0.150	0.002	0.006
A2	0.800	1.050	0.031	0.041
b	0.190	0.300	0.007	0.012
c	0.090	0.200	0.004	0.008
D	4.860	5.100	0.191	0.201
E	4.300	4.500	0.169	0.177
E1	6.200	6.600	0.244	0.260
e	0.650 BSC		0.026 BSC	
L	0.500	0.700	0.02	0.028
H	0.25 TYP		0.01 TYP	
θ	1°	7°	1°	7°

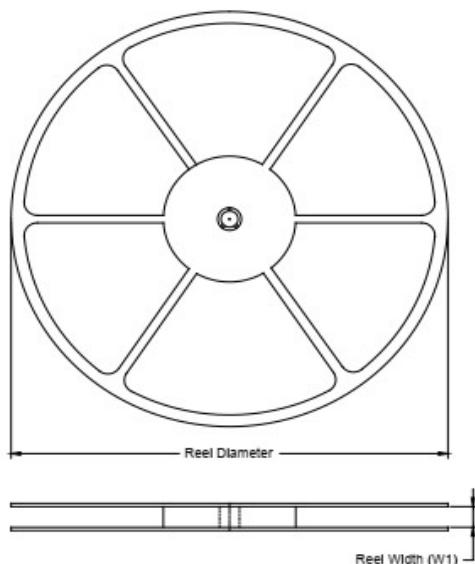
#### NOTES:

1. Body dimensions do not include mode flash or protrusion.
2. This drawing is subject to change without notice.

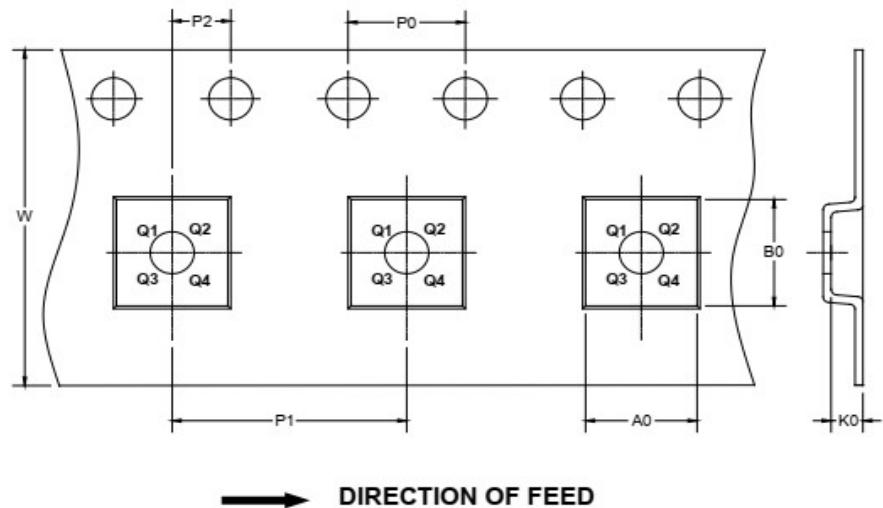
## Dual 4-Channel Analog Multiplexer/Demultiplexer

### TAPE AND REEL INFORMATION

#### REEL DIMENSIONS



#### TAPE DIMENSIONS



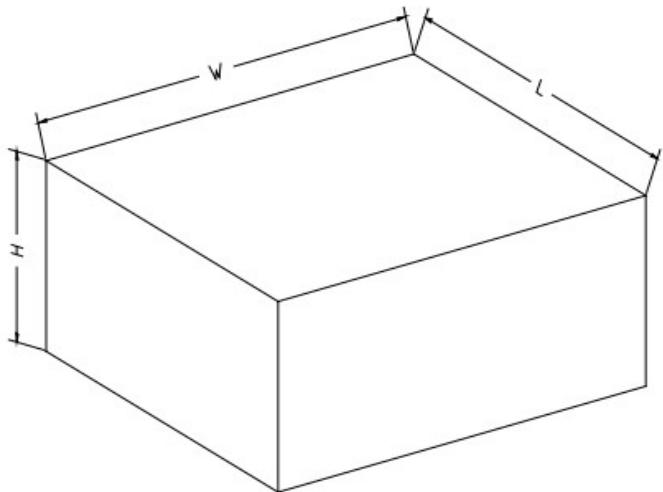
NOTE: The picture is only for reference. Please make the object as the standard.

#### KEY PARAMETER LIST OF TAPE AND REEL

Package Type	Reel Diameter	Reel Width W1 (mm)	A0 (mm)	B0 (mm)	K0 (mm)	P0 (mm)	P1 (mm)	P2 (mm)	W (mm)	Pin1 Quadrant
SOIC-16	13"	16.4	6.5	10.3	2.1	4.0	8.0	2.0	16.0	Q1
TSSOP-16	13"	12.4	6.9	5.6	1.2	4.0	8.0	2.0	12.0	Q1

## Dual 4-Channel Analog Multiplexer/Demultiplexer

### CARTON BOX DIMENSIONS



NOTE: The picture is only for reference. Please make the object as the standard.

### KEY PARAMETER LIST OF CARTON BOX

Reel Type	Length (mm)	Width (mm)	Height (mm)	Pizza/Carton
13"	386	280	370	5

## Dual 4-Channel Analog Multiplexer/Demultiplexer

### Statements And Notes

#### The name and content of Hazardous substances or Elements in the product

Part name	Hazardous substances or Elements									
	Lead and lead compounds	Mercury and mercury compounds	Cadmium and cadmium compounds	Hexavalent chromium compounds	Polybrominated biphenyls	Polybrominated biphenyl ethers	Dibutyl phthalate	Butylbenzyl phthalate	Di-2-ethylhexyl phthalate	Diisobutyl phthalate
Lead frame	o	o	o	o	o	o	o	o	o	o
PIsatic resin	o	o	o	o	o	o	o	o	o	o
Chip	o	o	o	o	o	o	o	o	o	o
The lead	o	o	o	o	o	o	o	o	o	o
Plastic sheet installed	o	o	o	o	o	o	o	o	o	o
explanation	o: Indicates that the content of hazardous substances or elements in the detection limit of the following the SJ/T11363-2006 standard. X: Indicates that the content of hazardous substances or elements exceeding the SJ/T11363-2006 Standard limit requirements.									

### Notion

Recommended carefully reading this information before the use of this product;

The information in this document are subject to change without notice;

This information is using to the reference only, the company is not responsible for any loss;

The company is not responsible for the any infringement of the third party patents or other rights of the responsibility.