

### **Features**

• Input Voltage Range: 4.0V to 24V

• Output Constant Current: 3A

Up to 95% efficiency

• High Efficiency ( >85% ) at light load

• 500KHz Switching Frequency

• Internal Soft-start

• Input under-voltage lockout

Current run-away protection

Output Short Protection

Thermal Shutdown

• Available in SOT23-6L

## **Applications**

Distributed Power Systems

Networking Systems

• FPGA, DSP, ASIC Power Supplies

Green Electronics Appliances

Notebook Computer

### **General Description**

The HCR3227 is a monolithic buck switching regulator based on I2 architecture for fast transient response.

Operating with an input range of 4V~24V.

the HCR3227 delivers 3A of continuous output current with two integrated N-Channel MOSFETs. The internal synchronous power switches provide high efficiency without the use of an external Schottky diode. At light loads, the regulator operates in low frequency to maintain high efficiency and low output ripples.

The HCR3227 guarantees robustness with output short protection, thermal protection, current run-away protection, input under voltage lockout.

The HCR3227 is available in SOT23-6L packages, which provide a compact solution with minimal external components.



SOT23-6L

Figure a. Package Type of HCR3227



## **Pin Configuration**

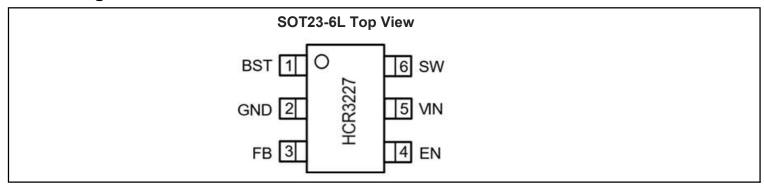
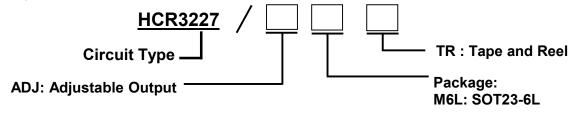


Figure b. Pin Configuration of HCR3227 (Top View )

### **Pin Function Table**

Pin Number	Pin Name	Function
1	BST	Connect a 0.1uF capacitor between BST and SW pin to supply voltage for the top switch driver.
2	GND	Ground Pin
3	FB	Output Feedback Pin. FB senses the output voltage and is regulated by the control loop to 0.6V. Connect a resistive divider at FB.
4	EN	Drive EN pin high to turn on the regulator and low to turn off the regulator.
5	VIN	Input voltage pin. VIN supplies power to the IC. Connect a 4V to 24V supply to VIN and bypass VIN to GND with a suitably large capacitor to eliminate noise on the input to the IC.
6	SW	SW is the switching node that supplies power to the output. Connect the output LC filter from SW to the output load.

## **Ordering Information**



### **Ordering Code**

Part Number	Marking ID <sup>note2</sup>	Temperature Range	Package	Quantity per Reel
HCR3227/ADJM6LTR	*W67XX	-40'C to +125'C	SOT23-6L	3000pcs /tape&Reel

Note 2: The "XX" is Inside code.



## **Functional Block Diagram**

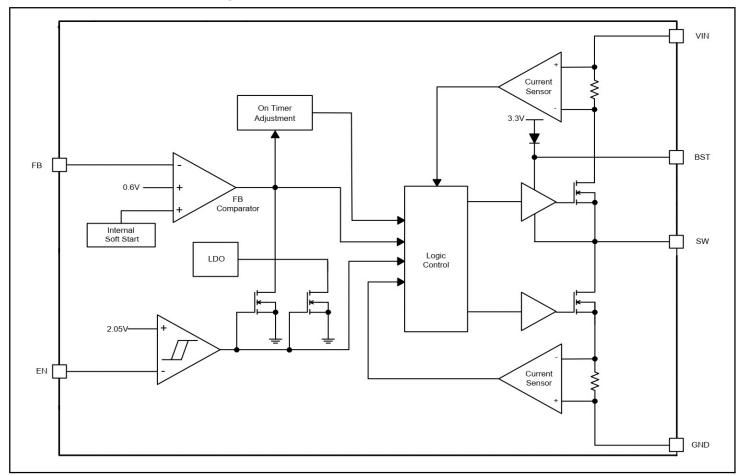


Figure c. Functional Block Diagram of HCR3227

## **Typical Application Circuit**

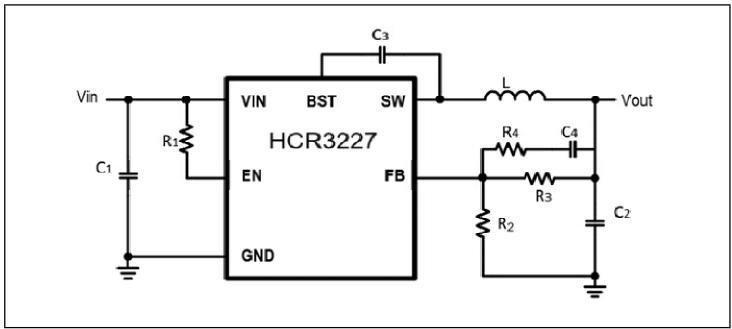


Figure d. Typical Application Circuit of HCR3227



## Absolute Maximum Ratings Note 1

Parameter	Symbol	Value	Unit
Input Supply Voltage Range	VIN	-0.3 to +25.0	v
SW Voltage Range	vsw	-0.3 to +25.0	V
EN Voltage Range	VEN	-0.3 to +25.0	V
BST Voltage Range	VBST	SW-0.3 to SW+5.0	V
All other Pins	<b>V</b> *	-0.3 to +6.0	V
Power Dissipation	РО	920	mW
Thermal Resistance Junction to Ambient <sup>note4</sup>	RθJA	110	'C/W
Thermal Resistance Junction to Case note4	RθJC	55	'C/W
Storage Temperature Range	тѕтс	-65 to 150	'C
Junction Temperature note2,3	TJ	150	'C
Lead Temperature (Soldering, 10s)	TLEAD	260	'C
ESD Susceptibility(Human Body Model)	НВМ	2	KV

- Note 1: Exceeding these ratings may damage the device.
  - 2: The HCR3227 guarantees robust performance from -40°C to 150°C junction temperature. The junction temperature range specification is assured by design, characterization and correlation with statistical process controls.
  - 3: The HCR3227 includes thermal protection that is intended to protect the device in overload conditions. Thermal protection is active when junction temperature exceeds the maximum operating junction temperature. Continuous operation over the specified absolute maximum operating junction temperature may damage the device.
  - 4: Measured on JESD51-7, 4-layer PCB

## **Recommended Operating Conditions**

Parameter	Symbol	Test Condition	Min	Туре	Max	Unit
Input Voltage Range	VIN		4.0	-	24	V
Output Voltage	VOUT		0.6	-	VIN-3	V
Operating Junction Temperature Range	TJ		-40	-	125	'C



## **Electrical Characteristics**

(VIN=12V, TA=25'C, unless otherwise stated.)

se stated./					
Symbol	Test Condition	Min	Туре	Max	Unit
VIN_MIN		3.3	3.6	3.8	٧
VIN_MIN_HYST		-	200	1	mV
lq	VEN=5.0V, VFB=1.2V	-	220	-	uA
Isd	VEN=0V	-	-	1	uA
VFB	T <sub>A</sub> =25'C, 4V <vin<24v< td=""><td>588</td><td>600</td><td>612</td><td>mV</td></vin<24v<>	588	600	612	mV
IFB	V <sub>FB</sub> =0.6V	-	-	120	uA
Rds(on)t		-	70	-	mΩ
Rds(on)B		-	38	1	mΩ
ILEAK_TOP	VIN=24V, VEN=0V, VSW=0V	-	-	1.0	uA
ILEAK_BOT	VIN=24V, VEN=0V, Vsw=24V	-	-	1.0	uA
ILIM_TOP	Minimum Duty Cycle	5	6	7	Α
ILIM_BOT	Minimum Duty Cycle	2.8	3.4	4.1	Α
Fosc	V <sub>FB</sub> =0.6V	-	500	-	KHz
η	V <sub>FB</sub> =0.6V	-	95	-	%
VEN-H	VEN rising	1.9	2.05	2.2	٧
VEN-HYS	Ven Hysteresis	-	150	-	mV
Ton		-	120	-	nS
Toff	V <sub>FB</sub> =0.4V	-	100	•	nS
Tss		-	1.6	-	mS
TTSD		-	140	1	ċ
TTSD_HYST		-	15	-	'C
	Symbol VIN_MIN VIN_MIN_HYST IQ ISD VFB IFB RDS(ON)T RDS(ON)B ILEAK_TOP ILEAK_BOT ILIM_TOP ILIM_BOT FOSC      VEN-H VEN-HYS  TON Toff Tss TTSD	Symbol Test Condition  VIN_MIN  VIN_MIN_HYST  IQ VEN=5.0V, VFB=1.2V  ISD VEN=0V  VFB TA=25'C, 4V <vin<24v cycle="" duty="" fosc="" hysteresis="" ifb="" ileak_top="" ilim_bot="" ilim_top="" minimum="" rds(on)b="" rds(on)t="" rising="" td="" toff="" ton="" tss="" ttsd<="" ven="" ven-h="" ven-hys="" vfb="0.4V" vin="24V," vsw="0V"><td>Symbol         Test Condition         Min           VIN_MIN         3.3           VIN_MIN_HYST         -           IQ         VEN=5.0V, VFB=1.2V         -           ISD         VEN=0V         -           VFB         TA=25'C, 4V<vin<24v< td="">         588           IFB         VFB=0.6V         -           RDS(ON)T         -         -           RDS(ON)B         -         -           ILEAK_TOP         VIN=24V, VEN=0V, VSW=0V         -           ILEAK_BOT         VIN=24V, VEN=0V, VSW=24V         -           ILIM_TOP         Minimum Duty Cycle         5           ILIM_BOT         Minimum Duty Cycle         2.8           FOSC         VFB=0.6V         -           VEN-H         VEN rising         1.9           VEN-HYS         VEN Hysteresis         -           TON         -         -           Toff         VFB=0.4V         -           TSS         -         -           TTSD         -         -</vin<24v<></td><td>Symbol         Test Condition         Min         Type           VIN_MIN         3.3         3.6           VIN_MIN_HYST         -         200           IQ         VEN=5.0V, VFB=1.2V         -         220           ISD         VEN=0V         -         -           VFB         TA=25°C, 4V<vin<24v< td="">         588         600           IFB         VFB=0.6V         -         -           RDS(ON)T         -         70         RDS(ON)B         -         38           ILEAK_TOP         VIN=24V, VEN=0V, VSW=0V         -         -         -           ILEAK_BOT         VIN=24V, VEN=0V, VSW=24V         -         -         -           ILIM_TOP         Minimum Duty Cycle         5         6         -           ILIM_BOT         Minimum Duty Cycle         2.8         3.4         -         500         -         -         500         -         95         -         VEN-H         VEN Pise-0.6V         -         95         VEN-H         VEN Hysteresis         -         150         -         120         -         120         -         100         -         1.6         -         140         -         140</vin<24v<></td><td>Symbol         Test Condition         Min         Type         Max           VIN_MIN         3.3         3.6         3.8           VIN_MIN_HYST         -         200         -           IQ         VEN=5.0V, VFB=1.2V         -         220         -           ISD         VEN=0V         -         -         1           VFB         TA=25'C, 4V<vin<24v< td="">         588         600         612           IFB         VFB=0.6V         -         -         120           RDS(ON)T         -         70         -           RDS(ON)B         -         38         -           ILEAK_TOP         VIN=24V, VEN=0V, VSW=0V         -         -         1.0           ILEAK_BOT         VIN=24V, VEN=0V, VSW=24V         -         -         1.0           ILIM_TOP         Minimum Duty Cycle         5         6         7           ILIM_BOT         Minimum Duty Cycle         2.8         3.4         4.1           FOSC         VFB=0.6V         -         500         -           I         VFB=0.6V         -         95         -           VEN-H         VEN Hysteresis         -         150         -      <tr< td=""></tr<></vin<24v<></td></vin<24v>	Symbol         Test Condition         Min           VIN_MIN         3.3           VIN_MIN_HYST         -           IQ         VEN=5.0V, VFB=1.2V         -           ISD         VEN=0V         -           VFB         TA=25'C, 4V <vin<24v< td="">         588           IFB         VFB=0.6V         -           RDS(ON)T         -         -           RDS(ON)B         -         -           ILEAK_TOP         VIN=24V, VEN=0V, VSW=0V         -           ILEAK_BOT         VIN=24V, VEN=0V, VSW=24V         -           ILIM_TOP         Minimum Duty Cycle         5           ILIM_BOT         Minimum Duty Cycle         2.8           FOSC         VFB=0.6V         -           VEN-H         VEN rising         1.9           VEN-HYS         VEN Hysteresis         -           TON         -         -           Toff         VFB=0.4V         -           TSS         -         -           TTSD         -         -</vin<24v<>	Symbol         Test Condition         Min         Type           VIN_MIN         3.3         3.6           VIN_MIN_HYST         -         200           IQ         VEN=5.0V, VFB=1.2V         -         220           ISD         VEN=0V         -         -           VFB         TA=25°C, 4V <vin<24v< td="">         588         600           IFB         VFB=0.6V         -         -           RDS(ON)T         -         70         RDS(ON)B         -         38           ILEAK_TOP         VIN=24V, VEN=0V, VSW=0V         -         -         -           ILEAK_BOT         VIN=24V, VEN=0V, VSW=24V         -         -         -           ILIM_TOP         Minimum Duty Cycle         5         6         -           ILIM_BOT         Minimum Duty Cycle         2.8         3.4         -         500         -         -         500         -         95         -         VEN-H         VEN Pise-0.6V         -         95         VEN-H         VEN Hysteresis         -         150         -         120         -         120         -         100         -         1.6         -         140         -         140</vin<24v<>	Symbol         Test Condition         Min         Type         Max           VIN_MIN         3.3         3.6         3.8           VIN_MIN_HYST         -         200         -           IQ         VEN=5.0V, VFB=1.2V         -         220         -           ISD         VEN=0V         -         -         1           VFB         TA=25'C, 4V <vin<24v< td="">         588         600         612           IFB         VFB=0.6V         -         -         120           RDS(ON)T         -         70         -           RDS(ON)B         -         38         -           ILEAK_TOP         VIN=24V, VEN=0V, VSW=0V         -         -         1.0           ILEAK_BOT         VIN=24V, VEN=0V, VSW=24V         -         -         1.0           ILIM_TOP         Minimum Duty Cycle         5         6         7           ILIM_BOT         Minimum Duty Cycle         2.8         3.4         4.1           FOSC         VFB=0.6V         -         500         -           I         VFB=0.6V         -         95         -           VEN-H         VEN Hysteresis         -         150         -      <tr< td=""></tr<></vin<24v<>

Note 5. Guaranteed by design.



### **Typical Performance Characteristics**

(Test condition: VIN=12V, VOUT=3.3V, L=4.7uH, Cout=22uF, TA=25'C, unless otherwise noted.)

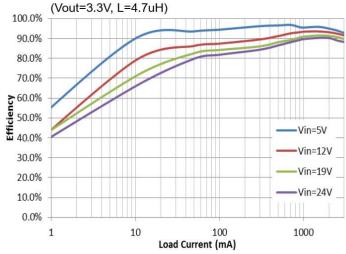


Figure 1. Efficiency vs Load Current

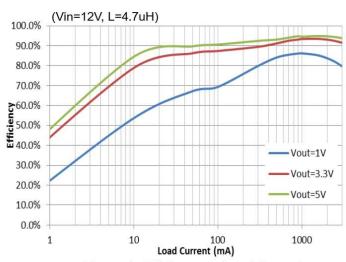


Figure 2. Efficiency vs Load Current

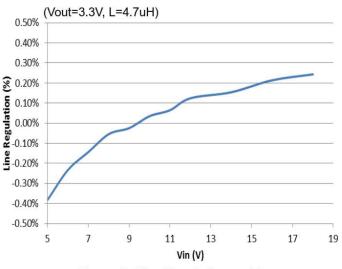


Figure 3. Line Regulation vs Vin

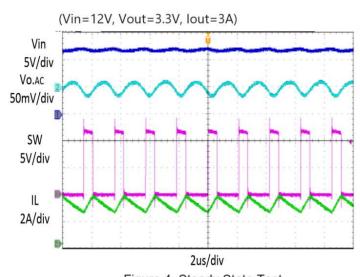


Figure 4. Steady State Test

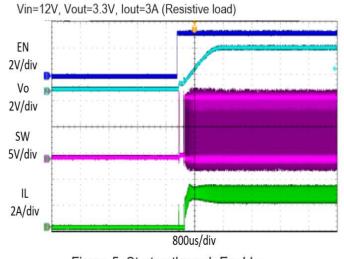


Figure 5. Startup through Enable

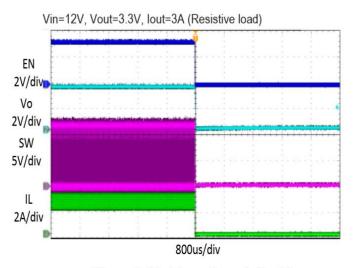


Figure 6. Shutdown through Enable



### **Typical Performance Characteristics(Con.)**

(Test condition: VIN=12V, VOUT=3.3V, L=4.7uH, Cout=22uF, TA=25'C, unless otherwise noted.)

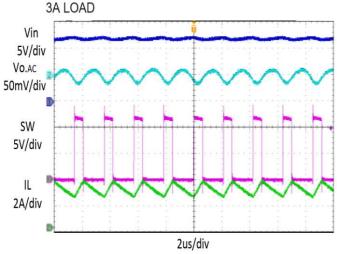


Figure 7. Heavy Load Operation

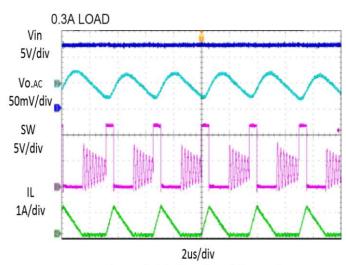


Figure 8. Medium Load Operation

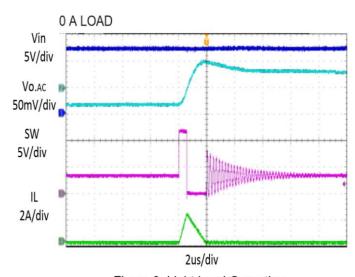


Figure 9. Light Load Operation

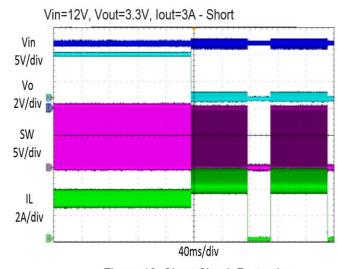


Figure 10. Short Circuit Protection

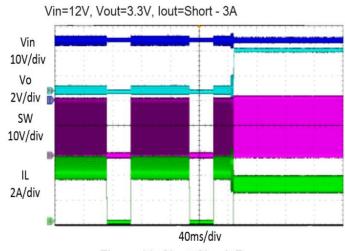


Figure 11. Short Circuit Recovery

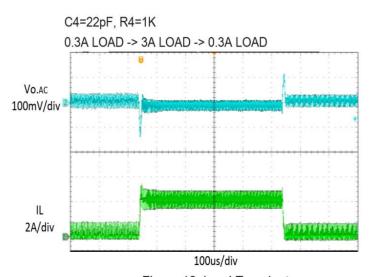


Figure 12. Load Transient



### **FUNCTIONAL DESCRIPTION**

The HCR3227 is a synchronous step-down regulator based on I2 control architecture. It regulates input voltages from 4V to 24V down to an output voltage as low as 0.6V, and is capable of supplying up to 3A of load current.

#### **Shut-Down Mode**

HCR3227 shuts down when voltage at EN pin is driven below 0.3V. The entire regulator is off and the supply current consumed by HCR3227 drops below 1uA.

#### **Power Switch**

N-Channel MOSFET switches are integrated on the HCR3227 to down convert the input voltage to the regulated output voltage. Since the top MOSFET needs a gate voltage great than the input voltage, a boost capacitor connected between BST and SW pins is required to drive the gate of the top switch. The boost capacitor is charged by the internal 3.7V rail when SW is LOW.

#### Vin Under-Voltage Protection

A resistive divider can be connected between Vin and ground, with the central tap connected to EN, so that when Vin drops to the pre-set value, EN drops below 1.9V to trigger input under voltage lockout protection.

#### **Output Current Run-Away Protection**

At start-up, due to the high voltage at input and low voltage at output, current inertia of the output inductor can be easily built up, resulting in a large start-up output current. A valley current limit is designed in the HCR3227 so that only when output current drops below the valley current limit can the top power switch be turned on. By such control mechanism, the output current at start-up is well controlled.

#### **Output Short Protection**

When the output is shorted to ground, the regulator is allowed to switch for 1024 cycles. If the short condition is cleared within this period, then the regulator resumes normal operation. If the short condition is still present after 1024 switching cycles, then no switching is allowed and the regulator enters hiccup mode for 2048 cycles. After the 2048 hiccup cycles, the regulator will try to start-up again. If the short condition still exists after 1024 cycles of switching, the regulator enters hiccup mode. This process of start-up and hiccup iterate itself until the short condition is removed.

#### **Thermal Protection**

When the temperature of the HCR3227 rises above 140°C, it is forced into thermal shut-down. Only when core temperature drops below 125°C can the regulator becomes active again.

### APPLICATION INFORMATION

#### Output Voltage Set

The output voltage is determined by the resistor divider connected at the FB pin, and the voltage ratio is:

$$v_{FB} = v_{OUT} \cdot \frac{R_2}{R_2 + R_3}$$

where VFB is the feedback voltage and VOUT is the output voltage.

Choose R2 around  $10k\Omega\sim15k\Omega$ , and then R3 can be calculated by:

$$R_3 = \left(\frac{V_{OUT}}{0.6} - 1\right) \cdot R_2$$

The following table lists the recommended values.

## **Output Voltage Set (Con.)**

VOUT(V)	R3(KΩ)	R2(KΩ)
2.5	47	15
3.3	49.9	11
5.0	110	15
	F8 HCR3227 R2	

Figure e-1. Output Voltage Set



## **APPLICATION INFORMATION(Con.)**

### **Input Capacitor**

The input capacitor is used to supply the AC input current to the step-down converter and maintaining the DC input voltage. The ripple current through the input capacitor can be calculated by:

$$I_{C1} = I_{LOAD} \cdot \sqrt{\frac{v_{OUT}}{v_{IN}} \cdot \left(1 - \frac{v_{OUT}}{v_{IN}}\right)}$$

where ILOAD is the load current, VOUT is the output voltage, VIN is the input voltage.

The input capacitor can be calculated by the following equation when the input ripple voltage is determined.

$$C_{1} = \frac{I_{LOAD}}{f_{s} \cdot \Delta V_{IN}} \cdot \frac{V_{OUT}}{V_{IN}} \cdot \left(1 - \frac{V_{OUT}}{V_{IN}}\right)$$

where C1 is the input capacitance value, fs is the switching frequency, ∆VIN is the input ripple voltage. The input capacitor can be electrolytic, tantalum or ceramic. To minimize the potential noise, a small X5R or X7R ceramic capacitor, e.g.0.1uF, should be placed as close to the IC as possible when using electroly capacitors. A 22uF/25V ceramic capacitor is recommended in typical application.

#### **Output Capacitor**

The output capacitor is required to maintain the DC output voltage, and the capacitance value determines the output ripple voltage. The output voltage ripple can be calculated by:

$$\Delta V_{\text{OUT}} = \frac{V_{\text{OUT}}}{f_{\text{S}} \cdot L} \cdot \left(1 - \frac{V_{\text{OUT}}}{V_{\text{IN}}}\right) \cdot \left(R_{\text{ESR}} + \frac{1}{8 \cdot f_{\text{S}} \cdot C_2}\right)$$

where C2 is the output capacitance value and RESR is the equivalent series resistance value of the output capacitor.

The output capacitor can be low ESR electrolytic, tantalum or ceramic, and lower ESR capacitors get lower output ripple voltage.

The output capacitors also affect the system stability and transient response, and a 47uF ceramic capacitor is recommended in typical application.

#### Inductor

The inductor is used to supply constant current to the output load, and the value determines the ripple current

### Inductor(Con.)

which affect the efficiency and the output voltage ripple The ripple current is switch current limit, thus the inductance value can be calculated by:

$$L = \frac{V_{OUT}}{f_{s} \cdot \Delta I_{L}} \cdot \left(1 - \frac{V_{OUT}}{V_{IN}}\right)$$

where VIN is the input voltage, VOUT is the output voltage fs is the switching frequency, and  $\Delta$ IL is the peak-to-peak inductor ripple current.

### **External Bootstrap Capacitor**

A bootstrap capacitor is required to supply voltage to the top switch driver. A 0.1uF low ESR ceramic capacitor is recommended to be connected between the BST pin and SW pin.

#### **Feedforward Capacitor**

In order to minimize the ripple of output voltage at light load, a feedforward capacitor in series with a resistor should be in parallel to the upper divider resistor. Choose R4 around  $1k\Omega$  and C4 around 22pF.

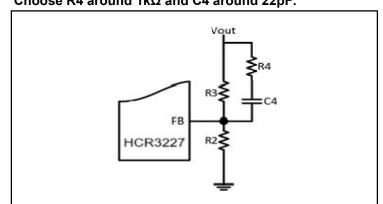


Figure e-2. Feedforward Capacitor

### Start up through EN

If HCR3227 start up through EN, a 10nF or larger capacitor should be connected between EN pin and GND to eliminate noise.

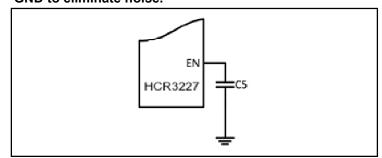


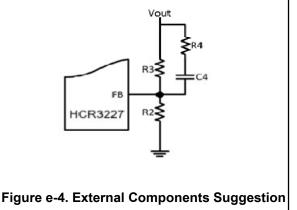
Figure e-3. Start up through EN



## **APPLICATION INFORMATION(Con.)**

### **External Components Suggestion:**

Vоит	R2	R3	R4	C4	L	Cout
(V)	(ΚΩ)	(ΚΩ)	(ΚΩ)	(pF)	(uH)	(uF)
1.0	13.3	9	1	180	4.7	54~66
1.2	28	28	1	180	4.7	54~66
1.5	16	24	1	180	4.7	54~66
2.5	15	47	1	22	4.7	22~66
3.3	11	49.9	1	22	4.7	22~66
5.0	15	110	1	22	4.7	22~66

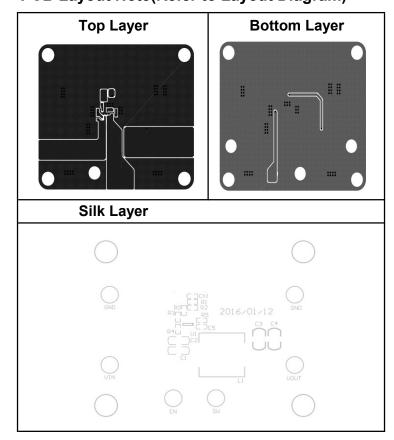


### **PCB Layout Note**

For minimum noise problem and best operating performance, the PCB is preferred to following the guidelines as reference.

- a-1. Place the input decoupling capacitor as close to the HCR3227 (VIN pin and PGND) as possible to eliminate noise at the input pin.
  - The loop area formed by input capacitor and GND must be minimized.
- a-2. Put the feedback trace as far away from the inductor and noisy power traces as possible.
- a-3. The ground plane on the PCB should be as large as possible for better heat dissipation.

### PCB Layout Note(Refer to Layout Diagram)



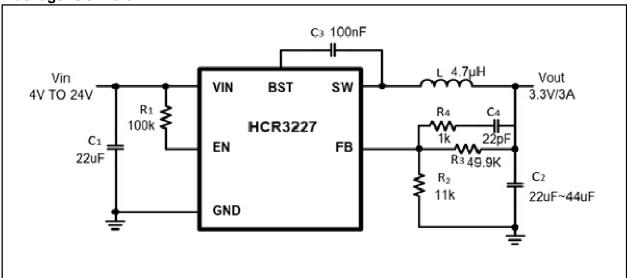


### **Refer to Design**

Reference-1

Vin : 4V ~ 24V Vout : 3.3V lout : 0 ~ 3A

Package: SOT23-6L

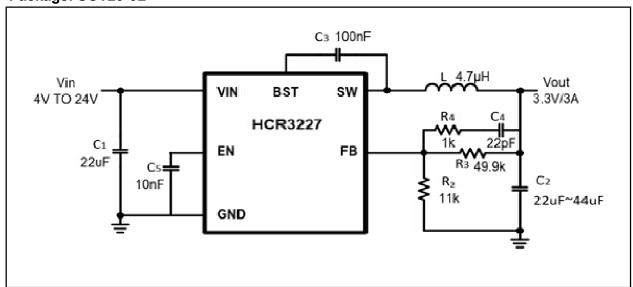


## Start up Through EN

Vin : 4V ~ 24V

Vout : 3.3V lout : 0 ~ 3A

Package: SOT23-6L

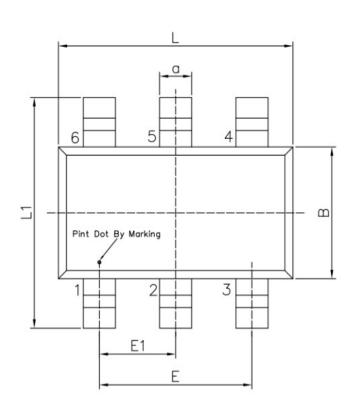


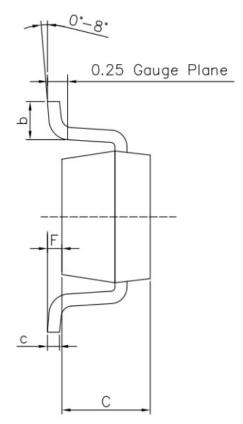


## **Mechanical Dimensions**

M6L PKG: SOT23-6L

Unit: mm





Cumbal	Dimensions I	n Millimeters	Cumahal	Dimensions In Millimeters		
Symbol	Min	Max	Symbol	Min	Max	
L	2.82	3.02	E1	0.85	1.05	
В	1.50	1.70	a	0.35	0.50	
С	0.90	1.30	С	0.10	0.20	
L1	2.60	3.00	b	0.35	0.55	
E	1.80	2.00	F	0	0.15	

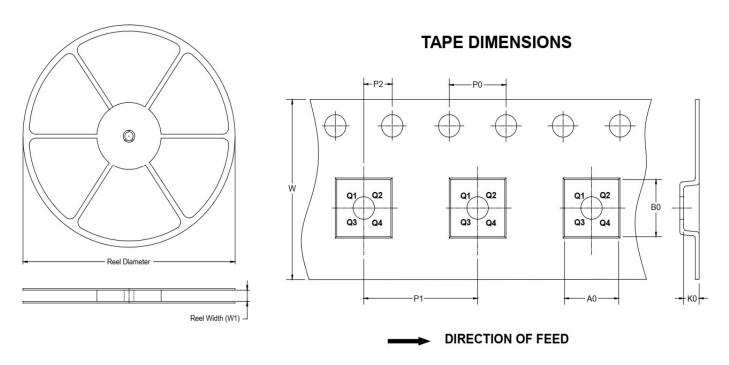
#### Note:

- 1) All dimensions are in millimeters.
- 2) Package length does not include mold flash, protrusion or gate burr.
- 3) Package width does not include inter lead flash or protrusion.
- 4) Lead popularity (bottom of leads after forming) shall be 0.10 millimeters max.
- 5) Pin 1 is lower left pin when reading top mark from left to right.



## TAPE AND REEL INFORMATION

### **REEL DIMENSIONS**



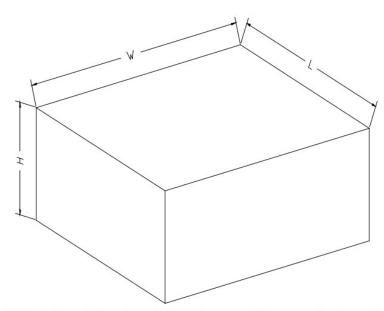
NOTE: The picture is only for reference. Please make the object as the standard.

### **KEY PARAMETER LIST OF TAPE AND REEL**

Package Type	Reel Diameter	Reel Width W1 (mm)	A0 (mm)	B0 (mm)	K0 (mm)	P0 (mm)	P1 (mm)	P2 (mm)	W (mm)	Pin1 Quadrant
SOT23-6L	7"	9.5	3.20	3.20	1.40	4.0	4.0	2.0	8.0	<b>Q</b> 3



## **CARTON BOX DIMENSIONS**



NOTE: The picture is only for reference. Please make the object as the standard.

### **KEY PARAMETER LIST OF CARTON BOX**

Reel Type	Length (mm)	Width (mm)	Height (mm)	Pizza/Carton
7"	442	410	224	18